



TRANSMITTAL FORM

FORM

(to be used for all correspondence after initial filing)

Application Number	09/928,737
Filing Date	August 13, 2001
First Named Inventor	Son K. Quan
Group Art Unit	2831
Examiner Name	Hung V. Ngo
Attorney Docket Number	SC09785T CD1

Total Number of Pages in this Submission 9 Attorney Docket Number SC09785T CD1					
		ENCLOSURES	(check a	ll that apply)	
X Fee Transmittal For	m	Drawing(s)		owance Communication to	
Fee Attached		Licensing-Related paper		Communication to Board	
Amendment/Reply		Petition	Appeal C Group	Communication to	
After Final		Petition to Convert to a	I — ` * * * * * * * * * * * * * * * * * *	Notice, Brief, Reply Brief) ary Information	
Affidavits/De	eclaration(s)	Provisional Application Power of Attorney,	Status Let	tter with appropriate copies	
Extension of Time I	Request	Revocation, Change of Correspondence Address	below)	osure(s) (please identify	
Express Abandonme	ent Request	Terminal Disclaimer	Return Pos	etcard	
X Information Disclos Form PTO/SB/08	sure Statement &	Request for Refund			
Certified Copy of P Documents	riority	CD, Number of CDs	_		
Response to Missing Incomplete Applica	-	Remarks			
Response	to Missing Parts				
	CFR 1.52 or 1.53	ATTORN	EV OD ACENT	2	
	IGNATURE O	OF APPLICANT, ATTORN	EY, OR AGENI	<u> </u>	
Firm or Robert	L. King		Registration No.	30,185	
Signature	Robert L. King Robert L. King Registration No. 30,185 Signature Date //19/2005 CERTIFICATE OF MAILING				
Date	1/19/200	5			
	C	ERTIFICATE OF MAILI	NG		
I hereby certify that this co Service with sufficient post Alexandria, VA 22313-1450	age as first class r	eing facsimile transmitted to the US mail in an envelope addressed to: C I below:	PTO or deposited with Commissioner for Pate	the United States Postal nts, P. O. Box 1450,	
Typed or printed name	Pat Thomas	1			
Signature	Wat &	homas	Date	1-21-05	

OIP E				
2			Complete if Known	
JAN . 2 5 2005 gg) FEE		Application Number	09/928,737	
TENNICANIT	TAL	Filing Date	August 13, 2001	
Patent fee are subject to annual revision Applicant claims small entity status. See 37 CFR 1.27		First Named Inventor	Son K. Quan	
Applicant claims small entity s	status. See 37 CFR 1.27	Examiner Name	Hung V. Ngo	
		Group Art Unit	2831	
TOTAL AMOUNT OF PAYMENT	(\$) 180	Attorney Docket No.	SC09785T CD1	

METHOD OF PAYMENT (check all that apply)			FE	E CAL	CULATIO	N (continued)	
Check Credit card Money Order Other None	3. ADI	DITIONAL					
X Deposit Account:		rge tity	Sm <u>En</u>	nall <u>tity</u>			
Deposit Account Number 503079	Fee	Fee	Fee	Fee			
Deposit Account Name Freescale Semiconductor,	Code	(\$)	Code	(\$)	F	ee Description	
Inc.		.,		,			
						1 . CP (F1
The Director is authorized to: (check all that apply)	1051	130	2051	65	•	- late filing fee or oath	
X Credit any overpayments	1052 1053	50 130	2052 1053	25 130		 tate Provisional filing sh specification 	
Charge any additional fee(s) during the pendency of this application	1812	2520	1812	2520	•	request for ex parte	
Officing any additional rea(s) during the pendency of this approach	1012	2320	1012	2320	Reexamin		
Charge fees(s) indicated below, except for the filing fee to the above-identified deposit account.	1804	920*	1804	920*	Requesting Examiner	publication of SIR prior to action	
	1805	1840*	1805	1840*	Requesting Examiner	g publication of SIR after action	
FEE CALCULATION	1251	110	2251	55	Extension	for reply within first mont	h
	1252	420	2252	210	Extension fo	r reply within second month	
	1253	950	2253	475	Extension fo	r reply within third month	
1. BASIC FILING FEE	1254	1480	2254	740	Extension fo	r reply within fourth month	
	1255	2010	2255	1005	Extension	for reply within fifth mont	n
Large Entity Small Entity	1401	330	2401	165	Notice of A	Appeal	
Fee Fee Fee	1402	330	2402	165		ef in support of an appea	1
Code (\$) Code (\$) Fee Paid	1403	290	2403	145		or oral hearing institute a public use	
	1451	1510	1451	1510	proceeding	3	
1001 770 2001 385 Utility filing fee	1452	110	2452	55		revive – unavoidable	
1002 340 2002 170 Design filing fee	1453	1330	2453	665		revive – unintentional	
1003 530 2003 265 Plant filing fee	1501 1502	1330 480	2501 2502	665 240	•	e fee (or reissue)	
1004 780 2004 385 Reissue filing fee 1005 160 2005 80 Provisional filing fee	1502	640	2502	320	Design iss Plant issue		
1005 160 2005 80 Provisional filing fee	1460	130	1460	130		the Commissioner	
SUBTOTAL (1) (S)	1807	50	1807	50		g fee under 37 CFR 1.17(q)	
2. EXTRA CLAIM FEES	1806	180	1806	180	Submissio	n of IDS	180
Previously Extra Fee from	8021	40	8021	40	_	each patent assignment	
Paid** Claims below Fee Paid	1809	770	2809	385		(times number of properties) bmission after final	
Independent Claims - 3 = X 86 =	1810	770	2810	385		37 CFR § 1.129(a)) additional invention to be	
Multiple Dependent 280 =	1010	770	2010	303		(37 CFR § 1.129(b))	
Large Entity Small Entity Fee Fee Fee Fee	1801	770	2801	385	Request for (RCE)	or Continued Examination	
Code (\$) Code (\$) Fee Description	1802	900	1802	900	Request fo	or expedited examination	
1202 18 2202 9 Claims in excess of 20 1201 84 2201 42 Independent claims in excess of 3	Other fe	e (specify)			or a de	sign application	
1203 280 2203 140 Multiple dependent claim, if not paid	<u> </u>					-1/2	
1204 84 2204 42 * Reissue independent claims over original patent	<u> </u>						
1205 18 2205 9 *Reissue clalms in excess of 20 and over original patent							
SUBTOTAL (2) (\$)					SURT	OTAL (3) (S) 18	0
"or number previously paid, if greater; For Reissues, see above.	* Redu	ced by Bas	sic Filing	Fee Paid		OTAL (0)	
SUBMITTED BY					Complete	(if applicable)	
Name (Print/Type) Robert L. King	Registr	ation No.	30,1	85	Tele	ephone 51	2.996.6839
Signature Robert Z. Ky	<u>ر</u> نہ			D	ate	1/19/	2005
Organisary Control of the Control of							



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.:

Son K. Quan et al.

Group Art Unit:

Examiner:

2831

Hung V. Ngo

Date Filed:

Title:

09/928,737

August 13, 2001

SEMICONDUCTOR PACKAGE AND METHOD THEREFOR

CERTIFICATE OF MAILING

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING DEPOSITED WITH THE UNITED STATES POSTAL SERVICE AS FIRST CLASS MAIL IN AN ENVELOPE ADDRESSED TO ASSISTANT COMMISSIONER FOR PATENTS ALEXANDRIA, VA 22313, ON Pat Thomas

INFORMATION DISCLOSURE STATEMENT (IDS)

Commissioner For Patents P.O. Box 1450 Alexandria, VA 22313-1450

SIR:

In accordance with 37 C.F.R. §1.56 and in compliance with 37 C.F.R. §1.98, the references listed on attached Form PTO/SB/08 and/or subsequently identified herein, are being submitted herewith for consideration by the United States Patent and Trademark Office. The Office hereby waives the requirement under 37 CFR 1.98 (a)(2)(i) for submitting a copy of each cited U.S. patent and each U.S. patent application publication for all U.S. national patent applications filed after June 30, 2003 and for all international applications that have entered the national stage under 35 USC § 371 after June 30, 2003. See 37 CFR 1.491(b).

1.	COPIES		
	a.🖂	0 1, 1,	publication or that portion which caused it to be listed; and
		(iii) all other information or that portion which caus	ed it to be listed, is included herewith.
	b	herewith were previously cited by or submitted t	which are listed on PTO/SB/08 which are not enclosed to the PTO in one of the following applications which has
		been relied upon for an earlier filing date under 35	0.3.0. 9120.
		II S. Serial Number	U.S. Filing Date

CONCISE EXPLANATION OF THE RELEVANCE (check at least one box) 11.

- Except as may be indicated below in (b) of this section, all of the patents, publications or other information a. 🖂 are in the English language (concise explanation not required).
- b. 🔯 A concise explanation of the relevance of all patents, publications or other information listed that is not in the English language is as follows: English translation is provided for cited references D-12 and D-13.
- The following additional information is provided for the Examiner's consideration: c. \square

CROSS REFERENCE TO RELATED APPLICATION(S) III. \square

> The Examiner is advised that the following co-pending application(s) contain(s) subject matter that may be related to the present application. By bringing this (these) applications to the Examiner's attention, Applicant(s) does(do) not waive the confidentiality provisions of 35 U.S.C. §122.

01/28/2005 MGEBREM1 009894060503079

09928737

Filing Date

Art Unit

FEES

IV	a	IS BEING FILED UNDER 37 C.F.R. §1.97(b): (check one box) within three months of the filing date of a national application other than a continued prosecution application under § 1.53(d) (37 C.F.R. §1.97(b)(1)). No fee or statement is required. within three months of the date of entry of the national stage as set forth in § 1.491 in an international application (37 C.F.R. §1.97(b)(2)). No fee or statement is required. before the mailing date of a first Office Action on the merits (37 C.F.R. §1.97(b)(3)). No fee or statement is required. before the mailing date of a first Office Action after the filing of a request for continued examination under § 1.114 (37 C.F.R. § 1.97(b)(4)). No fee or statement is required.
V. 🗌	before the §1.311, o a. \square	IS BEING FILED UNDER 37 C.F.R. §1.97(c): (check one box) e mailing date of any of a Final Office Action under 37 C.F.R. §1.113, a Notice of Allowance under 37 C.F.R. r an action that otherwise closes prosecution in the application (See 37 C.F.R. §1.97(c)). No statement; therefore, charge Deposit Account 503079, Freescale Semiconductor, Inc. the fee set forth in 37 C.F.R. §1.17(p). See the statement below. No fee is required.
VI. 🛚	on or befo	IS BEING FILED UNDER 37 C.F.R. §1.97(d): ore payment of the issue fee and is accompanied by the following: a statement under 37 C.F.R. §1.97(e) as provided below; and charge Deposit Account 503079, Freescale Semiconductor, Inc. the petition fee set forth in §1.17(p).
VII.	The unde a. b. c.	ENT UNDER 37 C.F.R. §1.97(e) (check only one box, if applicable) rsigned hereby states that each item of information contained in the IDS was cited in a communication from a foreign Patent Office in a counterpart foreign application not more than three months prior to the filing of IDS; or no item of information contained in the IDS was cited in a communication from a foreign Patent Office in a counterpart foreign application, and to knowledge of the person signing the statement after making reasonable inquiry, no item of information contained in the IDS was known to any individual designated in 37 C.F.R. 1.56(c) more than three months prior to the filing of this statement, or some of the items of information contained in the IDS were cited in a communication from a foreign Patent Office. As to this information, the undersigned states that each item of information contained in the IDS was cited in a communication from a foreign Patent Office in a counterpart foreign application not more than three months prior to the filing of this IDS. As to the remaining information, the undersigned hereby states that no item of this remaining information contained in the IDS was cited in a communication from a foreign Patent Office in a counterpart foreign application or, to the knowledge of the person signing the statement after making reasonable inquiry, no item of information contained in the IDS was known to any individual designated in 37 C.F.R. 1.56(c) more than three months prior to the filing of this statement.
VIII.		A check in the amount of is enclosed for the above-identified fee(s). Please charge Deposit Account No. 503079, Freescale Semiconductor, Inc. in the amount of \$180.00 for the above-indicated fee(s). If Applicant has overlooked any additional fees, or if any overpayment has been made, the Commissioner is hereby authorized to credit or debit Deposit Account 503079, Freescale Semiconductor, Inc. Two Copies of this paper are attached for Deposit Account charges and debits.

The above references are being cited only in the interests of candor and without any admission that they constitute statutory prior art or contain matter which anticipates the invention or which would render the same obvious, either singly or in a combination, to a person of ordinary skill in the art.

If the Examiner has any questions concerning this IDS, he/she is requested to contact the undersigned. If it is determined that this IDS has been filed under the wrong rule, the PTO is requested to consider this IDS under the proper rule (with a petition if necessary) and charge the appropriate fee to Deposit Account No. 503079, Freescale Semiconductor, Inc.

Respectfully submitted, Son K. Quan et al.

Robert L. King

Attorney for Applicant(s)

Reg. No. 30,185

Tel. 512.996.6839

FREESCALE SEMICONDUCTOR, INC. Customer Number 23125

Enclosures:

☐ Information Disclosure

Statement by Applicant References D-1 thru D-30

Foreign Search Report

Other:



INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary) Sheet

Complete if Known			
Application Number 09/928,737			
Filing Date August 13, 2001			
First Named Inventor Son K. Quan et al.			
Group Art Unit 2831			
Examiner Name Hung V. Ngo			
Attorney Docket No. SC09785T CD1			

			U. S. PATENT D	OCUMENTS	
Examiner Initials*	Cite No. 1	Document Number Number -Kind Code² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevan Passages or Relevant Figures Appear
	D-1	5,981,314	11-09-1999	Glenn et al.	
_	D-2	5,976,912	11-02-1999	Fukutomi et al.	
-	D-3	5,729,437	06-17-1998	Hashimoto	
	D-4	5,450,283	09-12-1995	Lin et al.	
-10	D-5	5,280,193	01-18-1994	Lin et al.	
	D-6	4,890,383	01-02-1990	Lumbard et al.	
	D-7	4,530,152	07-23-1985	Roche et al.	
	D-8	3,606,673	09-21-1971	Overman	
	D-9	3,444,441	05-13-1969	Helda et al.	
	D-10	3,413,713	12-03-1968	Helda et al.	
	<u> </u>				

	FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. 1	Foreign Patent Document Country Code ³ Number ⁴ Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Ţ 6	
	D-11	EP 0 261 324 A1	03-30-1988	Bednarz et al.			
	D-12	JP 02-047855	02-16-1990	Akase			
	D-13	WO 95/26047	09-28-1995	Fukutomi et al.			
. <u></u>							

Examiner	Date	
Signature	Considered	

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation, if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ² See Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English Language Translation is attached.

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Complete if Known Application Number 09/928,737 INFORMATION DISCLOSURE Filing Date August 13, 2001 First Named Inventor Son K. Quan et al. STATEMENT BY APPLICANT Group Art Unit 2831 Examiner Name Hung V. Ngo (use as many sheets as necessary) Attorney Docket Number SC09785T CD1 Sheet 2 of 3

	NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No. 1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T2			
	D-14	HOTTA, Yuji et al.; "Foil covered PACkage (FPAC): A New Package concept"; Electronic Components & Techonogy Conference; 1996; Cover Page & pp 1258-1264; IEEE Catalog No. 96CH35931.				
	D-15	YIP, Laurene et al; "Package Warpage Evaluation for High Performance PQFP"; 45th Electronic Components and Technology Conference;1995; Cover Page & pp 229-233; IEEE Catalog No. 95CH3582-0.				
	D-16	HYSOL® FP4451 Flow Control Dam Product Bulletin; Dexter Electronic Materials Division; 2 pgs; March, 1995.				
	D-17	HYSOL® FP4650 Liquid Encapsulant Product Bulletin; Dexter Electronic Materials Division; 4 pgs; May, 1995.				
	D-18	BOUTIN, Lynda; "Mold Compound Study for Plastic Ball Grid Array Applications"; Proceedings of the Technical Program, NEPCON EAST '95"; June 12-15, 1995; Cover Page & pp 279-290; Boston.				
	D-19	BURKHART, Art et al.; "New Generation Encapsulants For Chips On Low Cost First Level Substrates"; Dexter Technical Paper; February, 1994; pp 1-8; USA				
	D-20	MANZIONE, Louis T.; "Plastic Packaging of Microelectronic Devices"; AT&T Bell Laboratories Publication; 1990; 4 pgs.				
	D-21	McPHERSON, J.W. et al; "A Novel Thermal Expansion Matched Heatspreader for Plastic Encapsulation of Silicon Chips"; 25th Annual Proceedings, Reliability Physics 1987; April 7-9, 1987; Cover Page & pp 224-228; IEEE Catalog No. 87CH2388-7.				
	D-22	MURPHY, William; "Custom Molded Cavities"; Technical Papers, Regional Technical Conference, Society of Plastics Engineers, Inc.; March 6-7, 1985; 6 pgs.				

Examiner	Date	
Signature	 Considered	

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation, if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ² See Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English Language Translation is attached.

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 Complete if Known

 Application Number
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 Filing Date
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 First Named Inventor
 Son K. Quan et al.

 Group Art Unit
 2831

 Examiner Name
 Hung V. Ngo

 Attorney Docket Number
 SC09785T CD1

(use as many sheets as necessary)		
Sheet	3	G

	NON PATENT LITERATURE DOCUMENTS	
Cite No. 1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	Ţ2
D-23	MANGES, Prof. Dr. Georg et al.; "Electric-discharge machining (EDM):; How to Make Injection Molds; 1983; 5 pgs; Hanser Publishers.	
D-24	SCHUMACHER, Dr. B.; "The Significance of Electrical Discharge Machining (EDM) IN Mold Making"; Mold-Making Handbook for the Plastics Engineer, Chapter 13, 1983; 17 pgs; MacMillan Publishers, NY.	
D-25	HULL, John L; "Equipment and Techniques for Plastic Encapsulation of Electrical and Electronic Devices"; 36th Annual Technical Conference, Society of Plastics Engineers; April 24-27 1978; USA.	
D-26	CORNER, Harold L.; "The Moldmaking Trades: The Key to Success or Failure of the Plastics Industry"; 36th Annual Technical Conference, Society of Plastics Engineers; April 24-27, 1978; Cover Page & pp 172-174; USA.	
D-27	FORTIN, M.J.; "Automated Rotary Transfer Encapsulation of Electronic Parts"; 28th Annual Technical Conference, Society of Plastics Engineers, May 4-7, 1970; Cover Page & pp 160-161; USA.	
D-28	KLUZ, John; "Methods of Producing Cores and Cavities"; Moldmaking and Die Cast Dies for Apprentice Training; Chapter VIII; 1967; National Tool, Die and Precision Machining Association; Washington, D.C.	
D-29	JAMESON; E.C.; "Electrical Discharge Machining of Mold Cavities ands Cores"; Technical Papers, SPE Regional Technical Conference: Tooling for Plastics-Design and Construction" September 30, 1965; Cover Page & pp 17-21; USA.	
D-30	ROSS, Milton I.; "Electric Discharge Cutting of Mold Components"; Technical Papers, SPE Regional Technical Conference-Advances in Moldmaking and Mold Design; April 17, 1964; Cover Page pp 14-18, Society of Plastics Engineers.	
	D-23 D-24 D-25 D-27 D-28 D-29	Cite No. Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the aten (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. D-23 MANGES, Prof. Dr. Georg et al.; "Electric-discharge machining (EDM):; How to Make Injection Molds; 1983; 5 pgs; Hanser Publishers. D-24 SCHUMACHER, Dr. B.; "The Significance of Electrical Discharge Machining (EDM) IN Mold Making"; Mold-Making Handbook for the Plastics Engineer, Chapter 13, 1983; 17 pgs; MacMillan Publishers, NY. D-25 HULL, John L; "Equipment and Techniques for Plastic Encapsulation of Electrical and Electronic Devices"; 36 th Annual Technical Conference, Society of Plastics Engineers; April 24-27 1978; USA. D-26 CORNER, Harold L.; "The Moldmaking Trades: The Key to Success or Failure of the Plastics Industry"; 36 th Annual Technical Conference, Society of Plastics Engineers; April 24-27, 1978; Cover Page & pp 172-174; USA. D-27 FORTIN, M.J.; "Automated Rotary Transfer Encapsulation of Electronic Parts"; 28 th Annual Technical Conference, Society of Plastics Engineers, May 4-7, 1970; Cover Page & pp 160-161; USA. D-28 KLUZ, John; "Methods of Producing Cores and Cavities"; Moldmaking and Die Cast Dies for Apprentice Training; Chapter VIII; 1967; National Tool, Die and Precision Machining Association; Washington, D.C. D-29 JAMESON; E.C.; "Electrical Discharge Machining of Mold Cavities ands Cores"; Technical Papers, SPE Regional Technical Conference: Tooling for Plastics-Design and Construction" September 30, 1965; Cover Page & pp 17-21; USA. D-30 ROSS, Milton I.; "Electric Discharge Cutting of Mold Components"; Technical Papers, SPE Regional Technical Conference-Advances in Moldmaking and Mold Design; April 17, 1964; Cover Page pp 14-18,

NON PATENT LITERATURE DOCUMENTS

Examiner	Date
Signature	Considered

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